

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10002217
	Filing Date		2001-11-01
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01207M	

U.S.PATENTS						
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	1	5781747	B1	1998-07-14	Smith et al.	Entire Document
	2	6211708	B1	2001-04-03	Klemmer	Entire Document
	3	6850554	B1	2005-02-01	Sha	Entire Document
	4	7138841	B1	2006-11-21	Li	Entire Document
	5	6828824	B1	2004-12-07	Betz et al.	Entire Document
	6	3600690	B1	1971-08-17	White, Colin Graham	Entire Document
	7	4380083	B1	1983-04-12	Andersson et al.	Entire Document
	8	5917356	B1	1999-06-29	Casal et al.	Entire Document

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9	7295049	B1	2007-11-13	Moyal et al.	Entire Document
10	5491458	B1	1996-02-13	McCune	Entire Document
11	5651035	B1	1997-07-22	Tozun	Entire Document
12	5886582	B1	1999-03-23	Stansell	Entire Document
13	5952888	B1	1999-09-14	Scott	Entire Document
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	1	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al., filed on 09/26/2001; 25 pages	<input type="checkbox"/>
	2	Application Number 09/842,966 (CD00195): "Precision Crystal Oscillator Circuit Used in Microcontroller," Monte Mar, filed on 04/25/2001; 28 pages	<input type="checkbox"/>

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1	Application Number 09/207,912 (CD98086): "Circuit(s), Architecture and Method(s) for Operating and/or Tuning a Ring Oscillator," Monte Mar, filed on 12/09/1998; 23 pages	<input type="checkbox"/>
2	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block," Monte Mar, filed on 08/29/2001; 46 pages	<input type="checkbox"/>
3	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module," Perrin et al., filed on 09/09/2002; 41 pages	<input type="checkbox"/>
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	1	405055842	JP	A1	1993-03-26	Kondo	Entire Document	<input type="checkbox"/>
	2	404083405	JP	A1	1992-03-17	Yasue	Entire Document	<input type="checkbox"/>
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	1	USPTO Application Number 09/912,768 (CD00200): "A Microcontroller having a Dual Mode Relax Oscillator that is Trimmable," James Shutt; filed on 07/24/2001; 33 pages	<input type="checkbox"/>
	2	USPTO Application Number 09/893,048 (CD00231): "A Microcontroller having an On-Chip High Gain Amplifier," Kutz et al., filed on 06/26/2001; 22 pages	<input type="checkbox"/>
	3	USPTO Application Number 09/826,397 (CD00190): "Method and Circuit for Allowing a Microprocessor to Change its Operating Frequency on-the-Fly," Bert Sullam, filed on 04/02/2001; 24 pages	<input type="checkbox"/>
	4	USPTO Application Number 09/887,955 (CD00197): "Novel Power on Reset Circuit for Microcontroller," Kutz et al., filed on 06/22/2001; 42 pages	<input type="checkbox"/>
	5	USPTO Application Number 10/001,478 (CD01213M): "In-Circuit Emulator and POD Synchronized Boot," Nemecek et al., filed on 11/01/2001; 44 pages	<input type="checkbox"/>
	6	USPTO Application Number 10/001,568 (CD01214M): "Combined In-Circuit Emulator and Programmer," Nemecek et al., filed on 11/01/2001; 47 pages	<input type="checkbox"/>
	7	USPTO Application Number 09/972,319 (CD00179): "Method for Applying Instructions to Microprocessor in Test Mode," Warren Snyder, filed on 10/05/2001; 31 pages	<input type="checkbox"/>
	8	USPTO Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer," Nemecek et al., filed on 11/14/2001; 46 pages	<input type="checkbox"/>
	9	USPTO Application Number 10/004,197 (CD01220M): "In-Circuit Emulator with Gatekeeper Based Halt Control," Nemecek et al., filed on 11/14/2001; 47 pages	<input type="checkbox"/>

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10	USPTO Application Number 10/001,477 (CD01203M): "Breakpoint Control in an In-Circuit Emulation System," Roe et al., filed on 11/01/2001; 43 pages	<input type="checkbox"/>
11	USPTO Application Number 10/000,383 (CD00189): "System and Method of Providing a Programmable Clock Architecture for an Advanced Microcontroller," Sullam et al., filed on 10/24/2001; 34 pages	<input type="checkbox"/>
12	USPTO Application Number 09/887,923 (CD00229): "Novel Method and System for Interacting between a Processor and a Power on Reset to Dynamically Control Power States in a Microcontroller," Kutz et al., filed on 06/22/2001; 44 pages	<input type="checkbox"/>
13	USPTO Application Number 09/855,868 (CD00181): "Protecting Access to Microcontroller Memory Blocks," Warren Snyder, filed on 05/14/2001; 28 pages	<input type="checkbox"/>
14	USPTO Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 05/09/2005; 1 page	<input type="checkbox"/>
15	USPTO Application Number 09/935,454 (CD00194): "Method and Apparatus for Local and Global Power Management in a Programmable Analog Circuit," Monte Mar, filed on 08/22/2001; 51 pages	<input type="checkbox"/>
16	USPTO Application Number 09/923,461 (CD00192): "Non-Interfering Multiply-Mac (Multiply Accumulate) Circuit," Warren Snyder, filed on 08/06/2001; 25 pages	<input type="checkbox"/>
17	USPTO Application Number 09/922,579 (CD00230): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 08/03/2001; 37 pages	<input type="checkbox"/>
18	USPTO Application Number 09/922,419 (CD00198): "A Power Supply Pump Circuit for a Microcontroller," Kutz et al., filed on 08/03/2001; 38 pages	<input type="checkbox"/>
19	USPTO Application Number 10/272,231 (CD00170C): "Digital Configurable Macro Architecture," Warren Snyder, filed on 10/15/2002; 36 pages	<input type="checkbox"/>
20	USPTO Application Number 09/977,111 (CD00202): "A Frequency Doubler Circuit with Trimmable Current Control," Shutt et al., filed on 10/11/2001; 35 pages	<input type="checkbox"/>

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21	USPTO Application Number 09/973,535 (CD00193): "Architecture for Decimation Algorithm," Warren Snyder, filed on 10/09/2001; 26 pages	<input type="checkbox"/>
22	USPTO Application Number 09/972,133 (CD00178): "Method for Entering Circuit Test Mode," Warren Snyder, filed on 10/05/2001; 30 pages	<input type="checkbox"/>
23	USPTO Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface," Warren Snyder, filed on 10/05/2001; 32 pages	<input type="checkbox"/>
24	USPTO Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability," Mar et al., filed on 09/19/2001; 28 pages	<input type="checkbox"/>
25	USPTO Application Number 10/011,214 (CD00191): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies," Sullam et al., filed on 10/25/2001; 49 pages	<input type="checkbox"/>

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1	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," 2001, IEEE; 6 pages	<input type="checkbox"/>
2	USPTO Application Number 09/989,815 (CD01166M): "A Data Driven Method and System for Monitoring Hardware Resource Usage for Programming an Electric Device," Bartz et al., filed on 11/19/2001; 36 pages	<input type="checkbox"/>
3	USPTO Application Number 09/275,336 (CD98087): "Programmable Oscillator Scheme," Mar et al., filed on 03/24/1999; 25 pages	<input type="checkbox"/>
4	USPTO Application Number 09/721,316 (CD98087C1): "Programmable Oscillator Scheme," Mar et al., filed on 11/22/2000; 26 pages	<input type="checkbox"/>
5	USPTO Application Number 10/324,455 (CD98087C2): "Programmable Oscillator Scheme," Mar et al., filed on 12/20/2002; 23 pages	<input type="checkbox"/>
6	USPTO Application Number 09/998,859 (CD01209M): "A System and a Method for Checking Lock Step Consistency between in Circuit Emulation and a Microcontroller while Debugging Process is in Progress," Craig Nemecek, filed on 11/15/2001; 33 pages	<input type="checkbox"/>
7	USPTO Application Number 09/998,834 (CD01221M): "A System and a Method for Communication between and Ice and a Production Microcontroller while in a Halt State," Craig Nemecek, filed on 11/15/2001; 33 pages	<input type="checkbox"/>
8	USPTO Application Number 10/113,065 (CD01215M): "System and Method for Automatically Matching Components in a Debugging System," Nemecek et al., filed on 03/29/2002; 32 pages	<input type="checkbox"/>
9	USPTO Application Number 09/989,574 (CD01164M): "Method and System for using a Graphics user Interface for Programming an Electronic Device," Bartz et al., filed on 11/19/2001; 43 pages	<input type="checkbox"/>
10	USPTO Application Number 09/989,816 (CD01165M): "Datasheet Browsing and Creation with Data-Driven Datasheet Tabs within a Microcontroller Design Tool," Bartz et al., filed on 11/19/2001; 55 pages	<input type="checkbox"/>

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1	USPTO Application Number 11/850,260 (CD2007006): "Circuit and Method for Improving the Accuracy of a Crystal-less Oscillator Having Dual-Frequency Modes," Wright et al., filed on 09/05/2007; 33 pages	<input type="checkbox"/>
2	USPTO Non-Final Rejection for Application Number 11/850,260 (CD2007006) dated 03/06/2009; 7 pages	<input type="checkbox"/>
3	USPTO Application Number 09/404,891 (CD99054): "Method, Architecture and Circuitry for Controlling Pulse Width in a Phase and/or Frequency Detector," Scott et al., filed on 09/24/1999; 17 pages	<input type="checkbox"/>
4	USPTO Application Number 10/871,582 (CD03036): "LVDS Input Circuit with Extended Common Mode Range," Reinschmidt et al., filed on 06/17/2004; 25 pages	<input type="checkbox"/>
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6	USPTO Application Number 09/849,164 (CD01035): "Reduced Static Phase Error CMOS PLL Charge Pump," Jonathon Stiff, filed on 05/04/2001; 30 pages	<input type="checkbox"/>
7	USPTO Application Number 10/305,589 (CD02181): "Current Controlled Delay Circuit," Jonathon Stiff, filed on 11/26/2002; 18 pages	<input type="checkbox"/>
8	USPTO Application Number 11/322,044 (CD05198): "Split charge pump PLL architecture," Jonathon Stiff, filed on 12/28/2005; 19 pages	<input type="checkbox"/>
9	USPTO Application Number 11/132,894 (CD04064): "Open Loop Bandwidth Test Architecture and Method for Phase Locked Loop (PLL)," Jonathon Stiff, filed on 05/19/2005; 38 pages	<input type="checkbox"/>
10	USPTO Application Number 12/218,404 (CD05028C1): "Voltage Controlled Oscillator Delay Cell and Method," Sivadasan et al., filed on 07/14/2008; 23 pages	<input type="checkbox"/>
11	USPTO Application Number 11/415,588 (CD05028): "Voltage Controlled Oscillator Delay Cell and Method," Sivadasan et al., filed on 05/01/2006; 24 pages	<input type="checkbox"/>

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12	USPTO Application Number 11/644,100 (CD05204): "Differential-to-single ended signal converter circuit and method," Jonathon Stiff, filed on 12/21/2006; 33 pages	<input type="checkbox"/>
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2	USPTO Notice of Allowance for Application Number 09/989,777 (CD01208M) dated 03/09/2009; 7 pages	<input type="checkbox"/>
3	USPTO Application Number 10/288,003 (CD02108): "Low Voltage Differential Signal Driver Circuit and Method," Roper et al., filed on 11/04/2002; 30 pages	<input type="checkbox"/>
4	USPTO Application Number 10/293,392 (CD02135): "Low Voltage Receiver Circuit and Method for Shifting the Differential Input Signals of the Receiver Depending on a Common Mode Voltage of the Input Signals," Maher et al., filed on 11/13/2002; 20 pages	<input type="checkbox"/>
5	USPTO Application Number 08/865,342 (CD95045C): "Programmable Clock Generator," Mann et al., filed on 05/29/1997; 41 pages	<input type="checkbox"/>
6	USPTO Application Number 09/943,149 (CD01085): "Method for Phase Locking in a Phase Lock Loop," Moyal et al., filed on 08/30/2001; 21 pages	<input type="checkbox"/>
7	USPTO Application Number 09/048,905 (CD95045): "Programmable Clock Generator," Mann et al., filed on 03/26/1998; 42 pages	<input type="checkbox"/>
8	USPTO Application Number 09/538,989 (CD98104): "Memory Based Phase Locked Loop," Rengarajan S. Krishnan, filed on 03/30/2000; 27 pages	<input type="checkbox"/>
9	USPTO Application Number 09/981,448 (CD01102): "Oscillator Tuning Method," Lane T. Hauck, filed on 10/17/2001; 28 pages	<input type="checkbox"/>
10	USPTO Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation," Paul H. Scott, filed on 03/29/1998; 35 pages	<input type="checkbox"/>
11	USPTO Application Number 09/216,460 (CD98099): "Circuit and Method for Controlling an Output of a Ring Oscillator," Abugharbieh et al., filed on 12/18/1998; 21 pages	<input type="checkbox"/>

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13	USPTO Application Number 09/471,576 (CD99062): "Reference-Free Clock Generation and Data Recovery PLL," Kamal Dalmia, filed on 12/23/1999; 30 pages	<input type="checkbox"/>
14	USPTO Application Number 10/083,442 (CD01264): "Method/Architecture for a Low Gain PLL with Wide Frequency Range," Meyers et al., filed on 02/26/2002; 28 pages	<input type="checkbox"/>
15	USPTO Application Number 09/470,665 (CD99065): "Digital Phase/Frequency Detector, and Clock Generator and Data Recovery PLL Containing the Same," Kamal Dalmia, filed on 12/23/1999; 26 pages	<input type="checkbox"/>
16	USPTO Application Number 09/893,161 (CD00094): "Architecture of a PLL with Dynamic Frequency Control on a PLD," Michael T. Moore, filed on 06/27/2001; 32 pages	<input type="checkbox"/>
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2	Burogs et al., "Power Converter Analysis and Design using Matlab: A Transfer Function Approach," Proceedings of IEEE International Symposium on Industrial Electronics 1998, Vol. 2; 6 pages	<input type="checkbox"/>
3	USPTO Application Number 09/989,761 (CD01179M): "Storing of global parameter defaults and using them over two or more design projects," Ogami et al., filed on 11/19/2001; 37 pages	<input type="checkbox"/>
4	USPTO Application Number 11/200,619 (CD04112): "Providing hardware independence to automate code generation of processing device firmware," Snyder et al., filed on 08/10/2005; 41 pages	<input type="checkbox"/>
5	USPTO Application Number 09/989,819 (CD01181M): "System and method for creating a boot file utilizing a boot template," Ogami et al., filed on 11/19/2001; 43 pages	<input type="checkbox"/>
6	USPTO Application Number 09/989,775 (CD01195M): "User defined names for registers in memory banks derived from configurations," Ogami et al., filed on 11/19/2001; 29 pages	<input type="checkbox"/>
7	USPTO Application Number 09/989,781 (CD01180M): "System and method for decoupling and iterating resources associated with a module," Ogami et al., filed on 11/19/2001; 40 pages	<input type="checkbox"/>
8	USPTO Application Number 10/109,979 (CD01211M): "Graphical user interface with logic unifying functions," Anderson et al., filed on 03/29/2002; 100 pages	<input type="checkbox"/>
9	USPTO Application Number 11/201,922 (CD04111): "Design model for a hardware device-independent method of defining embedded firmware for programmable systems," McDonald et al., filed on 08/10/2005; 31 pages	<input type="checkbox"/>
10	USPTO Application Number 09/989,808 (CD01169M): "Automatic generation of application program interfaces, source code, interrupts, and data sheets for microcontroller programming," Bartz et al., filed on 11/19/2001; 67 pages	<input type="checkbox"/>
11	USPTO Application Number 11/201,627 (CD04109): "Method and an apparatus to design a processing system using a graphical user interface," Ogami et al., filed on 08/10/2005; 37 pages	<input type="checkbox"/>

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Application Number	10002217
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01207M

1	USPTO Application Number 10/113,064 (CD01200M): "Method and System for Debugging through Supervisory Operating Codes and Self Modifying Codes," Roe et al., filed on 03/29/2002; 36 pages	<input type="checkbox"/>
2	USPTO Application Number 10/002,726 (CD01171M): "Method and Apparatus for Generating Microcontroller Configuration Information," Ogami et al., filed on 10/24/2001; 54 pages	<input type="checkbox"/>

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1	USPTO Notice of Allowance for Application Number 11/986,338 (CD1197C) dated 05/07/2009; 1 page	<input type="checkbox"/>
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4	USPTO Non-Final Rejection for Application Number 09/475,808 (CD99069) dated 06/06/2001; 8 pages	<input type="checkbox"/>
5	Ebling, "Gemini II: A Second Generation Layout Validation Program;" 1988; in proceedings of the IEEE International Conference on Computer Aided Design (ICCAD-88); 4 pages	<input type="checkbox"/>
6	USPTO Application Number 12/132,527 (CD01175C): "System and Method for Performing Next Placements and Pruning of Disallowed Placements for Programming an Integrated Circuit;" Ogami et al., filed on 06/03/2008; 44 pages	<input type="checkbox"/>
7	USPTO Application Number 09/475,808 (CD99069): "Configurable Memory for Programmable Logic Circuits," Lacey et al., filed on 12/30/1999; 24 pages	<input type="checkbox"/>

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1	USPTO Application Number 11/273,708 (CD05141): "Capacitance Sensor Using Relaxation Oscillators," Snyder et al., filed on 11/14/2005; 33 pages	<input type="checkbox"/>
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4	USPTO Application Number 11/985,340 (CD04036C1): "Method and Circuit for Rapid Alignment of Signals," Moyal et al., filed on 11/13/2007; 34 pages	<input type="checkbox"/>
5	USPTO Application Number 11/337,272 (CD05191): "Successive Approximate Capacitance Measurement Circuit," Warren Snyder, filed on 01/20/2006 29 pages	<input type="checkbox"/>
6	USPTO Non-Final Rejection for Application Number 11/088,028 (CD04036) dated 06/16/2006; 8 pages	<input type="checkbox"/>
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8	USPTO Notice of Allowance for Application Number 11/088,028 (CD04036) dated 07/02/2007; 6 pages	<input type="checkbox"/>
9	USPTO Application Number 11/088,028 (CD04036): "Method and Circuit for Rapid Alignment of Signals," Moyal et al., filed on 11/13/2007; 34 pages	<input type="checkbox"/>
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12	USPTO Application Number 11/983,291 (CD05191D): "Successive Approximate Capacitance Measurement Circuit," Warren Snyder, filed on 11/07/2007; 26 pages	<input type="checkbox"/>
13	USPTO Application Number 11/166,622 (CD05027): "Touch wake for electronic devices," Beard et al., filed on 06/23/2005; 22 pages	<input type="checkbox"/>
14	USPTO Application Number 11/698,660 (CD05176): "Configurable Bus," Kutz et al., filed on 01/25/2007; 35 pages	<input type="checkbox"/>
15	Sedra et al., "Microelectronic Circuits," 3rd Edition, Oxford University Press, 02/05/2007; 20 pages	<input type="checkbox"/>
16	USPTO Application Number 11/709,866 (CD05170): "Input/Output Multiplexer Bus," Dennis Sequine, filed on 02/21/2007; 33 pages	<input type="checkbox"/>

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U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6554469	B1	2003-04-29	Thomson et al.	Entire Document
	2	6149299	B1	2000-11-21	Aslan et al.	Entire Document
	3	6008685	B1	1999-12-28	Kunst, David J.	Entire Document
	4	6157024	B1	2000-12-05	Chapdelaine et al.	Entire Document
	5	7176701	B1	2007-02-13	Wachi et al.	Entire Document
	6	7281846	B1	2007-10-16	McLeod, Scott C.	Entire Document
	7	7176701	B1	2007-02-13	Wachi et al.	Entire Document
	8	7227389	B1	2007-06-05	Gong et al.	Entire Document

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	9	7091713	B1	2006-08-15	Erdelyi et al.	Entire Document
	10	6121791	B1	2000-09-19	Abbott	Entire Document
	11	6449628	B1	2002-09-10	Wasson	Entire Document
	12	5761128	B1	1998-06-02	Watanabe, Kazuo	Entire Document
	13	6208572	B1	2001-03-27	Adams et al.	Entire Document
	14	6407953	B1	2002-06-18	Cleeves, James M.	Entire Document
	15	6650581	B1	2003-11-18	Hong et al.	Entire Document
	16	6731552	B1	2004-05-04	Perner, Martin	Entire Document
	17	6862240	B1	2005-03-01	Burgan, John M.	Entire Document
	18	7301835	B1	2007-11-27	Joshi et al.	Entire Document
	19	4972372	B1	1990-11-20	Ueno, Kouji	Entire Document

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	20	7376001	B1	2008-05-20	Joshi et al.	Entire Document
	21	5463591	B1	1995-10-31	Aimoto et al.	Entire Document
	22	5963503	B1	1999-10-05	Lee, Young-Dae	Entire Document
	23	5896330	B1	1999-04-20	Gibson, Garnet Frederick Randall	Entire Document
	24	6577258	B1	2003-06-10	Ruha et al.	Entire Document
	25	5198817	B1	1993-03-30	Walden et al.	Entire Document
	26	7129873	B1	2006-10-30	Kawamura, Akinobu	Entire Document
	27	6183131	B1	2001-02-06	Holloway et al.	Entire Document
	28	7009444	B1	2006-03-07	Scott, Greg	Entire Document
	29	6853598	B1	2005-02-08	Chevallier, Christopher J.	Entire Document
	30	6215352	B1	2001-04-10	Sudo, Naoaki	Entire Document

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31	5530673	B1	1996-06-25	Tobita et al.	Entire Document
32	7358714	B1	2008-04-15	Watanabe et al.	Entire Document
33	7193901	B1	2007-03-20	Ruby et al.	Entire Document
34	7554847	B1	2009-06-30	Lee, Jong-Soo	Entire Document
35	6253754	B1	2001-07-03	Roohparvar	Entire Document
36	6101617	B1	2000-08-08	Burckhardt et al.	Entire Document
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2	20080259998	A1	2008-10-23	Venkataraman et al.	Entire Document
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6	USPTO Non-Final Rejection for Application Number 12/104,672 (CD2007138) dated 08/26/2009; 11 pages	<input type="checkbox"/>
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14	Shahbahrami et al., "Matrix Register File and Extended Subwords: Two Techniques for Embedded Media Processors," ACM, May 2005; 9 pages	<input type="checkbox"/>
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1	USPTO Notice of Allowance for Application Number 10/238,966 (CD01250M) dated 08/05/2009; 5 pages	<input type="checkbox"/>
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34	USPTO Non-Final Rejection for Application Number 11/166,622 (CD05027) dated 09/29/2009; 11 pages	<input type="checkbox"/>
35	USPTO Advisory Action for Application Number 11/166,622 (CD05027) dated 05/27/2009; 3 pages	<input type="checkbox"/>
36	USPTO Final Rejection for Application Number 11/166,622 (CD05027) dated 03/10/2009; 11 pages	<input type="checkbox"/>
37	USPTO Notice of Allowance for Application Number 12/218,404 (CD05028C1) dated 11/03/2009; 8 pages	<input type="checkbox"/>
38	USPTO Notice of Allowance for Application Number 12/218,404 (CD05028C1) dated 07/10/2009; 7 pages	<input type="checkbox"/>
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40	USPTO Non-Final Rejection for Application Number 11/698,660 (CD05176) dated 10/07/2009; 12 pages	<input type="checkbox"/>
41	USPTO Advisory Action for Application Number 11/698,660 (CD05176) dated 07/31/2009; 3 pages	<input type="checkbox"/>
42	USPTO Notice of Allowance for Application Number 11/983,291 (CD05191D) dated 10/22/2009; 7 pages	<input type="checkbox"/>
43	USPTO Final Rejection for Application Number 11/983,291 (CD05191D) dated 08/12/2009; 10 pages	<input type="checkbox"/>
44	USPTO Final Rejection for Application Number 11/322,044 (CD05198) dated 10/19/2009; 10 pages	<input type="checkbox"/>

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
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Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
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45	USPTO Final Rejection for Application Number 11/644,100 (CD05204) dated 08/19/2009; 12 pages	<input type="checkbox"/>
46	USPTO Advisory Action for Application Number 11/850,260 (CD2007006) dated 11/02/2009; 3 pages	<input type="checkbox"/>
47	USPTO Final Rejection for Application Number 11/850,260 (CD2007006) dated 08/21/2009; 9 pages	<input type="checkbox"/>
48	USPTO Final Rejection for Application Number 09/989,767 (CD01178) dated 12/07/2009; 22 pages	<input type="checkbox"/>

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